

FIG. 1

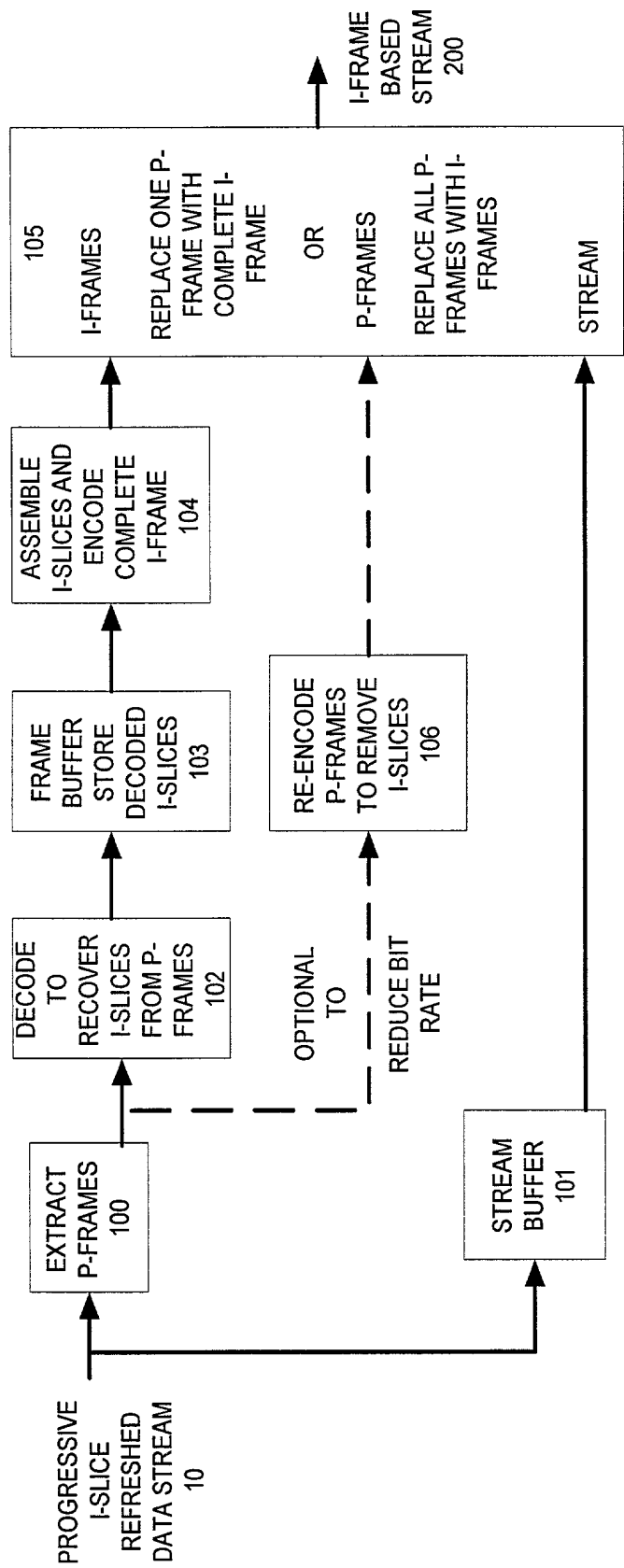


FIG. 2

FIG. 3 is a block diagram of a system 300 for processing a progressive I-slice refreshed MPEG data stream 10. The system 300 includes a receiver 210, a decoder 220, a processor 230, a memory 240, an encoder 250, and a multiplexer (MUX) 255. The receiver 210 receives the progressive I-slice refreshed MPEG data stream 10 and outputs data to the decoder 220. The decoder 220 is connected to the memory 240 and outputs data to the processor 230. The processor 230 is connected to the memory 240 and outputs data to the MUX 255. The MUX 255 outputs the I-frame based MPEG data stream 200. The processor 230 is also connected to an encoder 250, which outputs data to the MUX 255. The system 300 is enclosed in a dashed box.

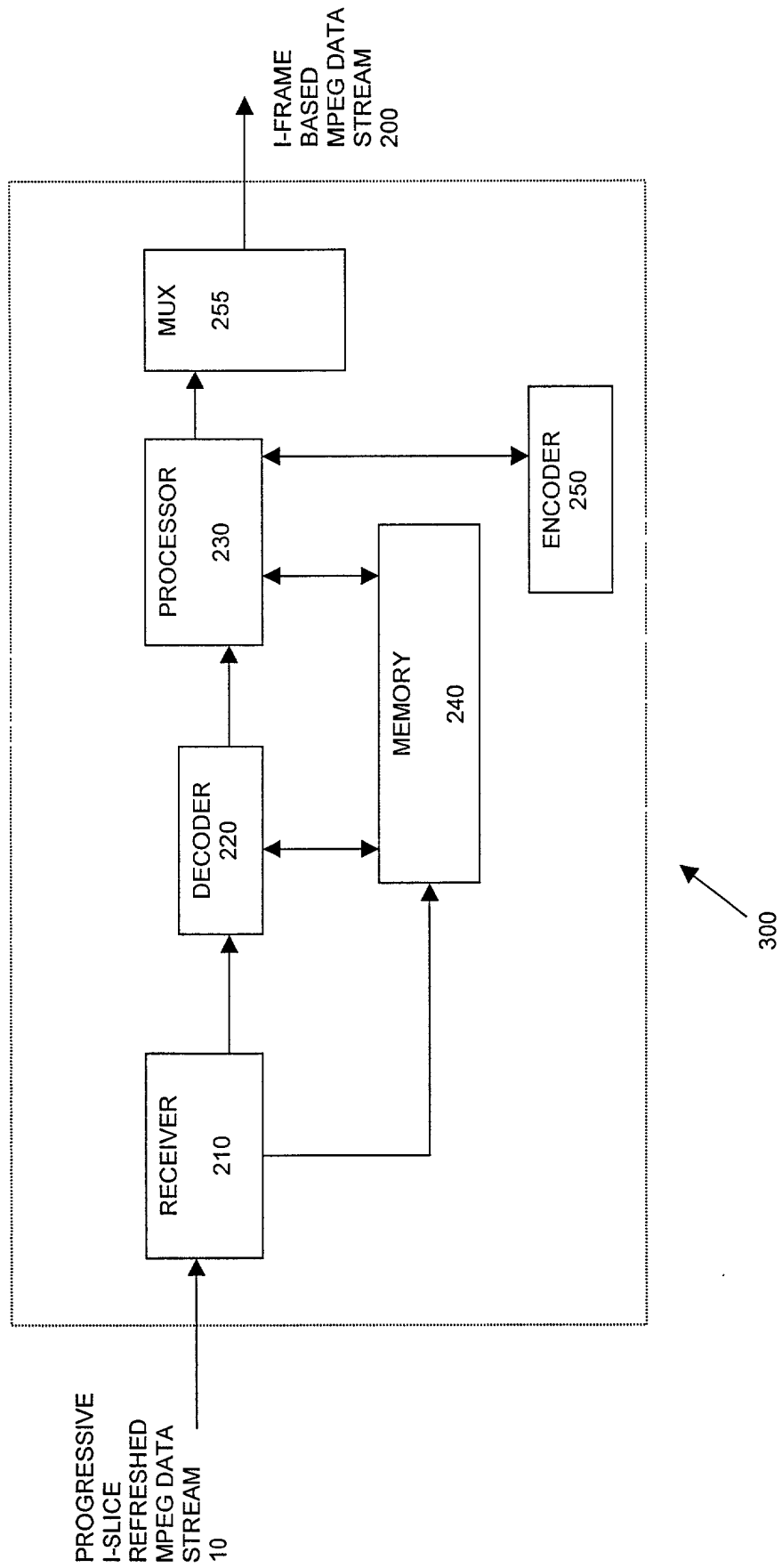


FIG. 3

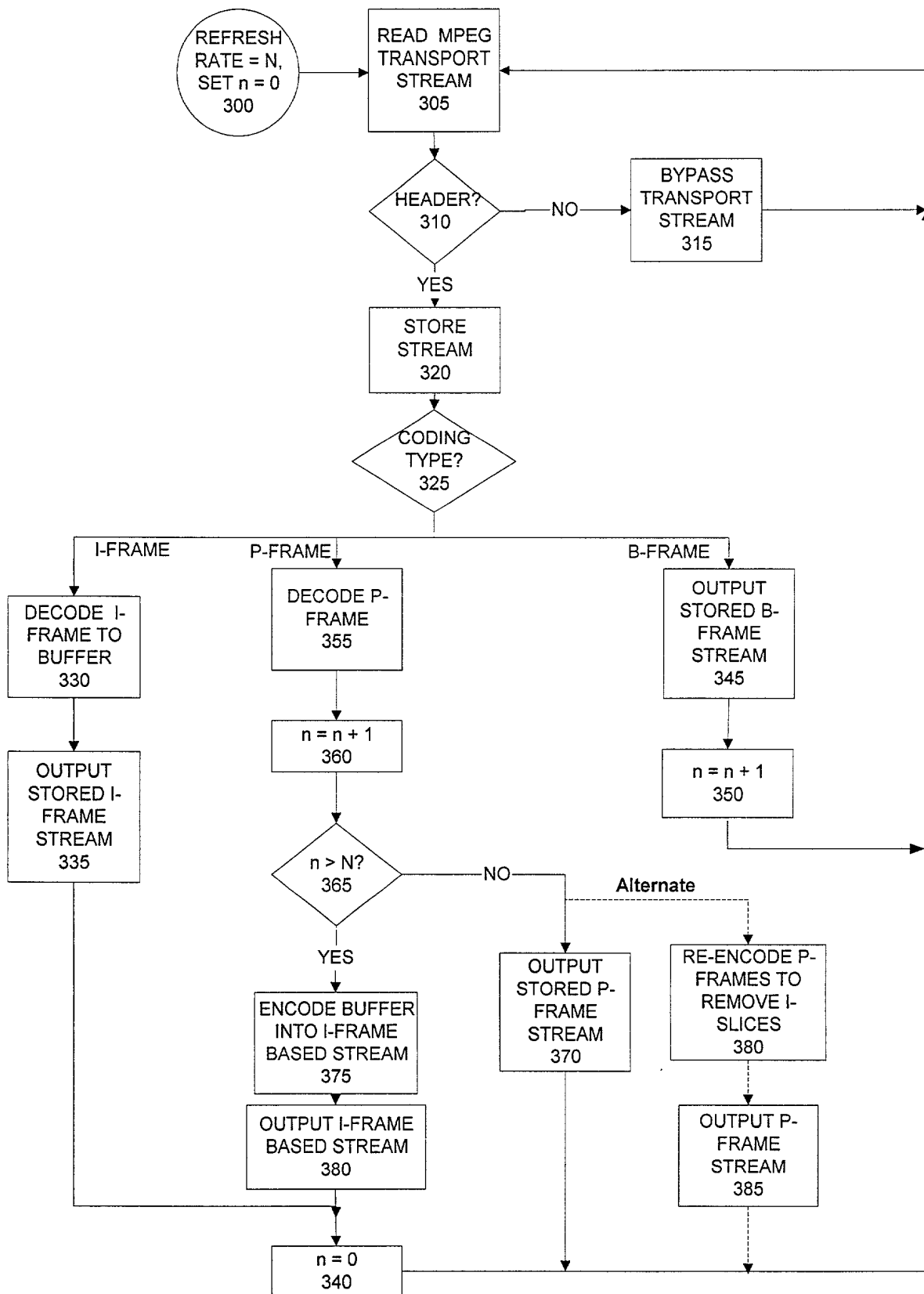


FIG. 4